



MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

Maisammguda, Dhulapally Post, Secunderabad 500100



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TIME TABLE

09.12.2024

YEAR: III	SECTION: A	SEMESTER-2	AY: 2024-25	ROOM NO: 5205
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DAY	1 9:20AM- 10:20AM	2 10:20AM- 11:20AM	3 11:30AM- 12:30PM	12:30PM- 1:30PM	4 1:30PM- 2:30PM	5 2:30PM- 3:30PM
Monday	VLSID	DSP	AWP	L U N C H	IOT-IA	FCS
Tuesday	FCS	VLSID	DSP		AWP	PDS
Wednesday	IOT-IA	DSP (B1) / VLSI (B2)			FCS	AD2
Thursday	FCS	PDS	AWP		DSP	VLSID
Friday	AWP	VLSID	IOT-IA		VLSI (B1) / DSP (B2)	
Saturday	AWP	FCS	DSP		IOT-IA	VLSID

SUBJECT CODE	SUBJECT	NAME OF THE FACULTY
R22A0412	Antennas and Wave Propagation	K. D. K AJAY
R22A0413	Digital Signal Processing	V.KIRAN KUMAR
R22A0414	VLSI Design	K. SURESH
R22A6951	Internet of Things & its applications	P. ANITHA
R22A6215	Fundamentals of Cyber Security	M. RAMANJANEYULU
R22A0490	Digital Signal Processing Laboratory	1. K. D. K AJAY 2. N. SARITHA
R22A0491	VLSI Design Laboratory	1. K. SURESH 2. NEHA THAKUR
R22A0492	Application Development-II	Dr SADANAND YADAV
R22A0085	Professional Development Skills-2	MR. B. ANJANEYULU
	Class Teacher	K. SURESH
	Year Coordinator	K. D. K AJAY

E. MAHENDER REDDY
V.SHIVARAJ KUMAR
TT I/C

Dr. MALLIKARJUNA LK
HOD

Dr. S. SRINIVASA RAO
PRINCIPAL



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TIME TABLE

09.12.2024

YEAR: III	SECTION: B	SEMESTER-2	AY: 2024-25	ROOM NO: 5208
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DAY	1 9:20AM- 10:20AM	2 10:20AM- 11:20AM	3 11:30AM 12:30PM	12:30P M- 1:30PM	4 1:30PM- 2:30PM	5 2:30PM- 3:30PM
Monday	FCS	IOT-IA	DSP	L U N C H	VLSID	AWP
Tuesday	IOT-IA	DSP	PDS		DSP (B1) / VLSI (B2)	
Wednesday	FCS	VLSID	IOT-IA		AWP	AD2
Thursday	AWP	VLSI (B1) / DSP (B2)			IOT-IA	VLSID
Friday	IOT-IA	FCS	AWP		VLSID	DSP
Saturday	VLSID	DSP	AWP		FCS	PDS

SUBJECT CODE	SUBJECT	NAME OF THE FACULTY
R22A0412	Antennas and Wave Propagation	K. D. K AJAY
R22A0413	Digital Signal Processing	V.KIRAN KUMAR
R22A0414	VLSI Design	Dr. ARUNKUMAR MADUPU
R22A6951	Internet of Things & its Applications	P. ANITHA
R22A6215	Fundamentals of Cyber Security	M. RAMANJANEYULU
R22A0490	Digital Signal Processing Laboratory	1. P. ANITHA 2. K. BHAVANA
R22A0491	VLSI Design Laboratory	1. Dr. ARUNKUMAR MADUPU 2. M. RAMANJANEYULU
R22A0492	Application Development-II	K VIJAYA BHARATHI
R22A0085	Professional Development Skills-2	MRS.PRIYAKULKARNI
	Class Teacher	K BHAVANA
	Year Coordinator	K. D. K AJAY

E. MAHENDER REDDY
V.SHIVARAJ KUMAR
TT I/C

Dr. MALLIKARJUNA LK
HOD

Dr. S. SRINIVASA RAO
PRINCIPAL